MR1115-381 Application Serial No. 10/082,321 Responsive to Official Action dated 5 January 2004

## **AMENDMENTS TO THE CLAIMS:**

The following Listing of Claims will replace all prior versions, and listings, of Claims in the Application:

## Listing of Claims:

Claims 1-5 (Canceled).

Claim 6 (Currently Amended): A method for forming spacers of at least two micro-displays of in a displaying device comprising at least two micro-display units, the method comprising the following steps of:

- (1) providing a substrate for each of the <u>said</u> at least two micro-displays <u>units</u> with reflective pads formed on each <u>said</u> substrate, the <u>reflective</u> pads being spaced from each other by non-reflective areas, each of the micro-displays <u>units</u> being associated with a different color whose light emissions together form a color image;
- (2) forming a coating of transparent, non-conductive material on each <u>said</u> substrate and over the reflective pads formed thereon;
- (3) providing a mask associated with each substrate, forming a set of at least two masks for said displaying device, each mask of said set of at least two masks being formed for a respective one of said at least two micro-display units of said

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displaying device and comprising a number of shielded zones, each of said number of shielded zones corresponding to predetermined a respective one of said non-reflective areas of a respective the substrate of said respective one of said at least two micro-display units, each of the said shielded zones of one mask of said set of at least two masks being positioned at different locations than the shielded zones of another mask of said set of at least two masks; and

(4) performing a lithographic operation on the said transparent, non-conductive coating of each said substrate of step (2) by using the associated mask of step (3) whereby a respective one of said set of at least two masks for each of said at least two micro-display units, wherein portions of the transparent, non-conductive material of on each said substrate corresponding to the shielded zones of the said respective mask associated therewith are left on the substrate and function as spacers, the spacers of one of said at least two micro-display units any one substrate not being positioned in a location corresponding to non-overlapping fashion with the spacers of another substrate of said at least two micro-display units.

Claim 7 (Original): The method as claimed in Claim 6, wherein the transparent, non-conductive material comprises SiO<sub>x</sub>.

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Claim 8 (Original): The method as claimed in Claim 7, wherein the SiO<sub>x</sub> comprises SiO<sub>2</sub>.

Claim 9 (Original): The method as claimed in Claim 6, wherein the transparent, non-conductive material comprises SiN<sub>x</sub>.

Claim 10 (Original): The method as claimed in Claim 9, wherein the  $SiN_x$  comprises  $SiN_2$ .

Claim 11 (Canceled).